

FOCUSED CALL FOR PAPERS

The 29th International Symposium on
Power Semiconductor Devices and ICs

ISPSD 2017

May 28 - June 1, 2017

Sapporo, Japan

Conference site: Royton Sapporo



ISPSD is pleased to announce increased focus on **POWER IC DESIGN and related topics**. Submissions covering one or more of the following topics are particularly encouraged. (See also 2nd call for paper.)

TOPICS OF INTEREST IN POWER IC DESIGN:

Circuit design and demonstration using power IC technology platform, including:

- ◆ Gate driver IC design. New circuit and layout design enhancing power IC performance
- ◆ Single chip inverters and converters
- ◆ New signal isolation technology on power IC
- ◆ Power SoC and passive component integration on a chip
- ◆ ESD protection circuit
- ◆ Compact circuit model for power IC design
- ◆ New type of hybrid power ICs
- ◆ Modeling, design platform and measurement technology for power IC

ABSTRACT SUBMISSION

- ◆ Prospective authors should visit the ISPSD 2017 website: <http://www.ispsd2017.com>).
- ◆ A PDF formatted abstract should be submitted through this website including:
 - A one page text summary in English (500 words maximum) and up to two pages of supporting figures and tables.
 - The abstract heading must include: Title, Authors, Affiliations, Postal Address, Phone/Fax numbers and Email address.
 - The abstracts must clearly state: Purpose of work, how the work is differentiated from prior work, specific results and their significance, and up-to-date references.
- ◆ **Charitat award:** If the first author as a presenter of a paper is no more than 30 years old at the time of the conference, their work will be considered for this award. The abstract must be identified at the time of submission.
- ◆ **Abstract submission deadline is NOVEMBER 15, 2016**
- ◆ Author notification of acceptance will be sent out by January 25, 2017
- ◆ Final paper submission dead line is March 31, 2017

General Chair: Dr. Mutsuhiro Mori, Hitachi, Ltd.

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